

Ubique Panel Control Board Datasheet

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Revision History

Table 1. Revision History

Revision	Description	Release Date
0.10	Initial release for draft version	December, 2020
0.20	Update for minor changes	December, 2020
0.30	Update for DC characteristics	December, 2020
0.40	Update information	January, 2021
0.50	Update pin description table	January, 2021
0.60	Update UART pin number	January, 2021
0.70	Update technical details	February, 2021
0.80	Update UART pin number	May, 2021
1.00	Update interface description and DC characteristics	July, 2021
1.10	Update pin configuration	July, 2021
1.20	Add CN5 pin configuration/definition	September, 2021

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1 Introduction

Ubique Panel product line provides total solutions for HMI. These solutions include LCM, Control Board and ADE (Acorn Development Environment). This document is to describe the functionality of the control boards of the Ubique Panel product line. These control boards include:

Table 2. Ubique Panel Product Line

Part Number	Resolution	Panel Size	Touch Type
IA204827PC-043	480x272	4.3"	CTP
IA208048PC-050	800x480	5.0"	CTP
IA208048PC-070	800x480	7.0"	CTP
IA210260PC-070	1024x600	7.0"	CTP

Note: CTP = Capacitive Touch Panel

These control boards must work with ADE to perform the HMI tasks designed in ADE. These control boards share the same hardware characteristics but are preloaded with different firmwares to accommodate different LCMs (Liquid Crystal display Modules).

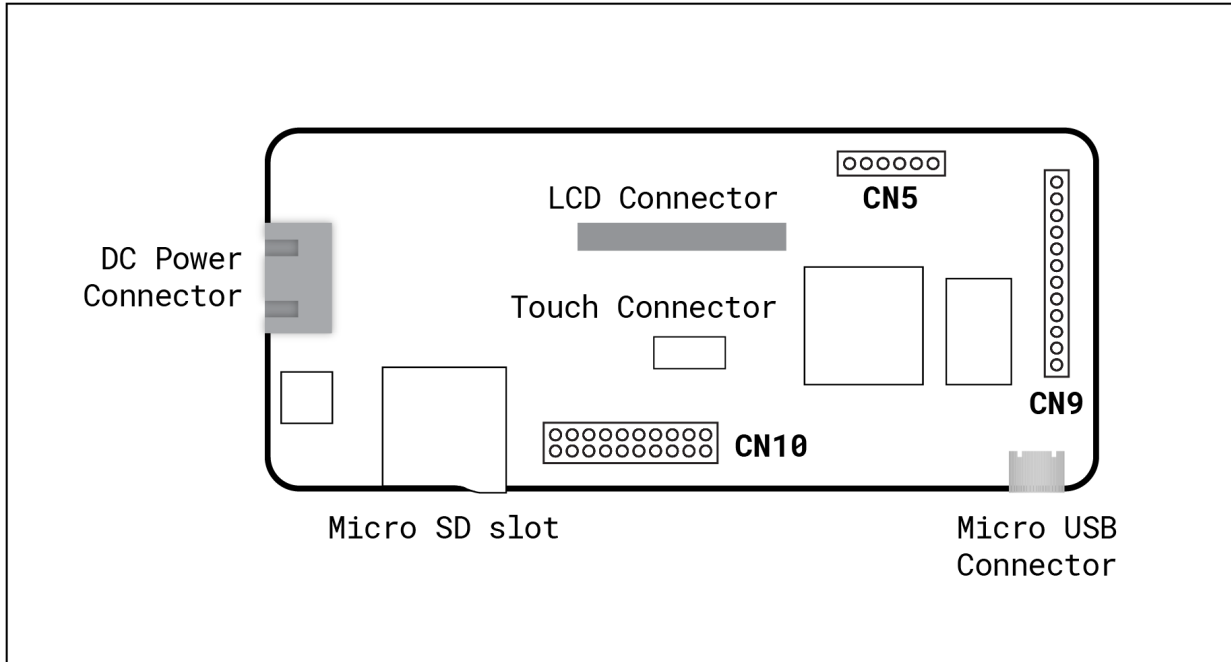
The LCMs are sourced from our partners and their corresponding datasheets are available separately in our website. Please visit <https://www.iot-hmi.com/> for more details.

1.1 Features

- Work only with ADE for delivering the Smart HMI total solution
- ARM Cortex M7-based MCU @480Mhz with 32MB DRAM
- Built-in 2Gb (256MB) Flash memory for storage
- Support ARGB888 (16,777,216 colors) with 256 levels of transparency
- Support LCM backlight control
- Support touch control (CTP)
- Support GPIO, PWM, Input-Capture and ADC functions
- Support input capture mode for frequency measurement
- Support UART communication interfaces
- Support USB interface for downloading designs from ADE

2 Interface Description

Figure 1. Control Board Outline



2.1 Connector 10 (CN10) Pin Definition

Figure 2. CN10 Pin Definition

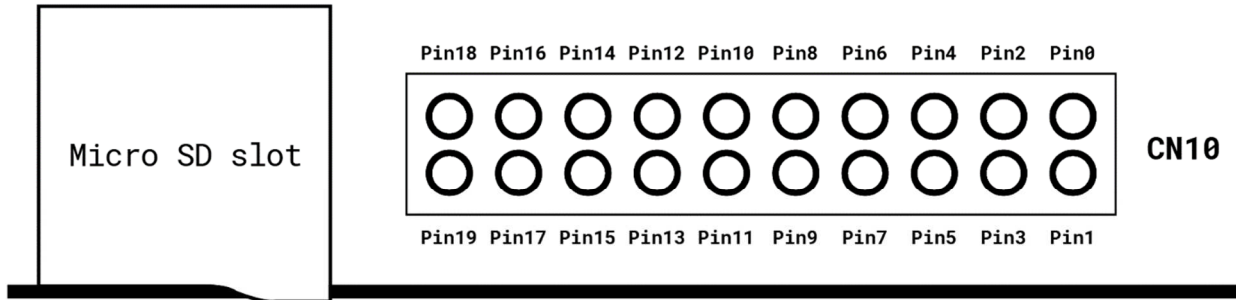


Table 3. CN10 Pin Definition

Pin Number	Name	I/O	Description
0	DC5V	P	DC 5V output.
1	GND	P	Supply Ground.
2	DC3.3V	P	DC 3.3V output.
3	GPIO1	I/O	General Purpose Input/Output, 3.3V Level - 5V Tolerant.
4	Reserved	N/A	Reserved.
5	GPIO2	I/O	General Purpose Input/Output, 3.3V Level - 5V Tolerant.
6	PWM4	I/O	This pin offers two different functions: GPIO and PWM. Users can select which function to be assigned to this pin inside ADE. For PWM, this pin generates digital pulses based on the parameters including frequency and duration set in the ADE.
7	GPIO3	I/O	General Purpose Input/Output, 3.3V Level - 5V Tolerant.
8	PWM5	I/O	This pin shares the same functionality as that of PWM4. Please refer to PWM4 description for details.
9	GPIO4	I/O	General Purpose Input/Output, 3.3V Level - 5V Tolerant.
10	PWM6	I/O	This pin shares the same functionality as that of PWM4. Please refer to PWM4 description for details.
11	GPIO5	I/O	General Purpose Input/Output, 3.3V Level - 5V Tolerant.
12	GND	P	Supply Ground.
13			

Pin Number	Name	I/O	Description
14	ADC0	I/O	General Purpose I/O pin with Analog Capability. This pin has a range of 0-3.3V when used as an Analog Input, and is 3.3V tolerant only.
15	ADC1	I/O	General Purpose I/O pin with Analog Capability. This pin has a range of 0-3.3V when used as an Analog Input, and is 3.3V tolerant only.
16	ADC2	I/O	General Purpose I/O pin with Analog Capability. This pin has a range of 0-3.3V when used as an Analog Input, and is 3.3V tolerant only.
17	ADC3	I/O	General Purpose I/O pin with Analog Capability. This pin has a range of 0-3.3V when used as an Analog Input, and is 3.3V tolerant only.
18	ADC4	I/O	General Purpose I/O pin with Analog Capability. This pin has a range of 0-3.3V when used as an Analog Input, and is 3.3V tolerant only.
19	ADC5	I/O	General Purpose I/O pin with Analog Capability. This pin has a range of 0-3.3V when used as an Analog Input, and is 3.3V tolerant only.

Note: I = Input, O = Output, P = Power

2.2 Connector 9 (CN9) Pin Definition

Figure 3. CN9 Pin Definition

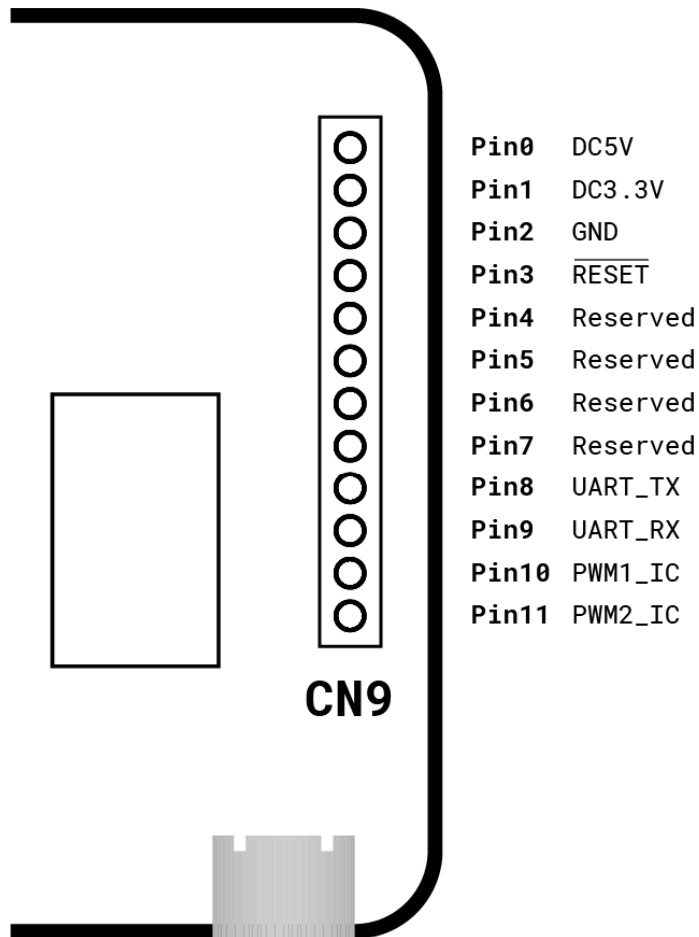


Table 4. CN9 Pin Definition

Pin Number	Name	I/O	Description
0	DC5V	P	DC 5V output.
1	DC3.3V	P	DC 3.3V output.
2	GND	P	Supply Ground.
3	$\overline{\text{RESET}}$	I	Reset signal. Active low.
4	Reserved	N/A	Reserved.
5	Reserved	N/A	Reserved.
6	Reserved	N/A	Reserved.
7	Reserved	N/A	Reserved.

Pin Number	Name	I/O	Description
8	UART_TX	I/O	TX for UART Asynchronous Serial Transmit pin, TTL level. Connect this pin to the Receive (Rx) signal of other serial devices. Used in conjunction with the UART_RX pin for communication with external MCU by UART Communication Protocol (The communication protocol is described in chapter UART Communication Protocol). This pin is tolerant up to 5.0V levels.
9	UART_RX	I/O	RX for UART Asynchronous Serial Receive pin, TTL level. Connect this pin to the Transmit (Tx) signal of other serial devices. Used in conjunction with the UART_TX pin for communication with external MCU by UART Communication Protocol (The communication protocol is described in chapter UART Communication Protocol). This pin is tolerant up to 5.0V levels.
10	PWM1_IC	I/O	This pin offers three different functions: GPIO, PWM and Input-capture. Users can select which function to be assigned to this pin inside ADE. For PWM, this pin generates digital pulses based on the parameters including frequency and duration set in the ADE. For the input-capture, the capture mode and the corresponding behavior are all set inside ADE.
11	PWM2_IC	I/O	This pin shares the same functionality as that of PWM1_IC. Please refer to PWM1_IC description for details.

Note: I = Input, O = Output, P = Power

2.3 DC Power Connector Pin Definition

Figure 4. DC Power Connector Pin Definition

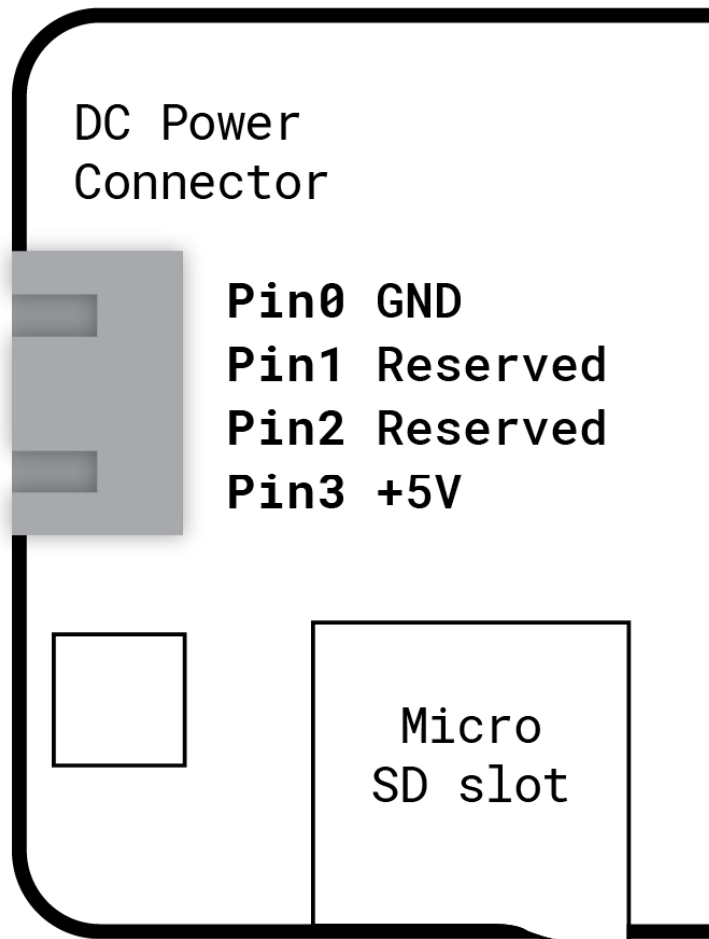


Table 5. DC Power Connector Pin Definition

Pin	Name	I/O	Description
0	GND	P	Supply Ground.
1	Reserved	N/A	Reserved.
2	Reserved	N/A	Reserved.
3	+5V	P	DC 5V input.

Note: I = Input, O = Output, P = Power

2.4 Connector 5 (CN5) Pin Definition

Figure 5. CN5 Pin Definition

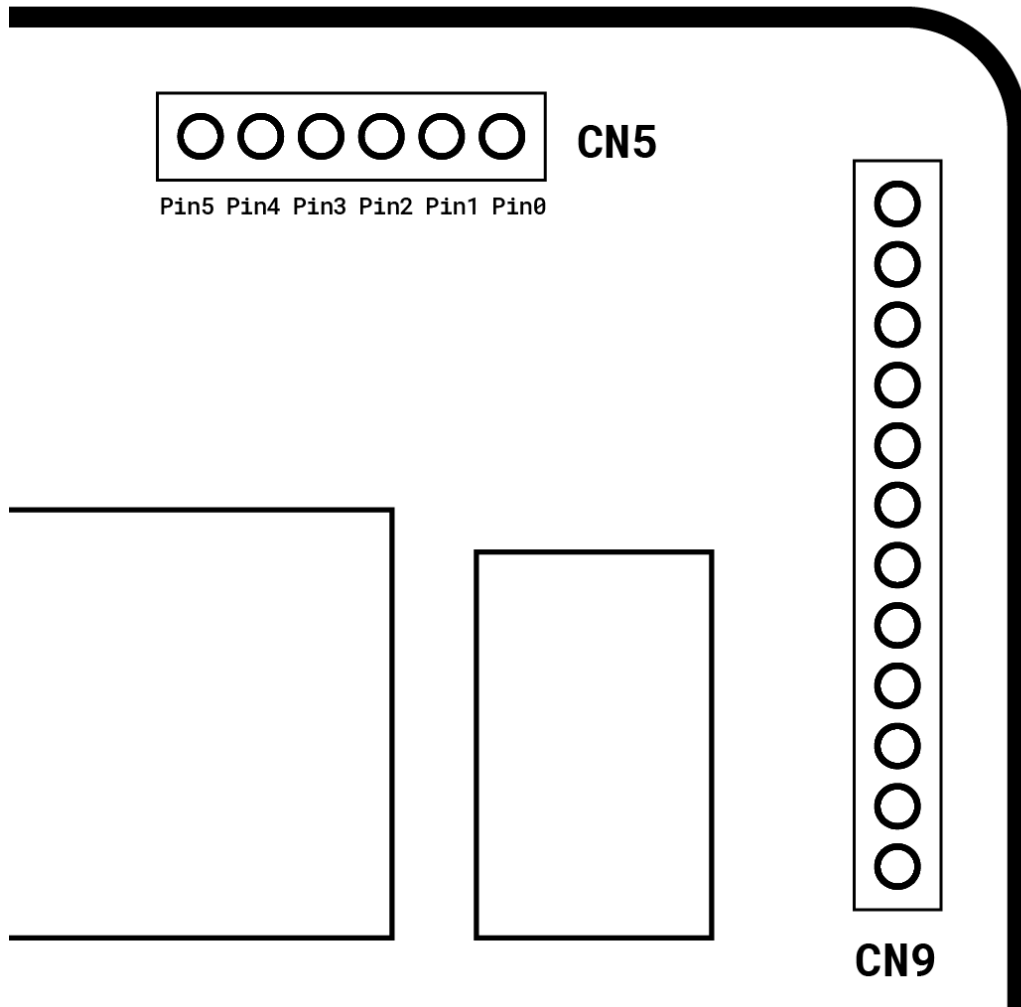


Table 6. CN5 Pin Definition

Pin	Name	I/O	Description
0	Reserved	N/A	Reserved.
1	Reserved	N/A	Reserved.
2	Reserved	N/A	Reserved.
3	Reserved	N/A	Reserved.
4	Reserved	N/A	Reserved.
5	Reserved	N/A	Reserved.

Note: I = Input, O = Output, P = Power

3 Absolute Maximum Ratings

Table 7. Absolute Maximum Ratings

Item	Minimum	Maximum	Unit
Storage temperature	-30	+80	°C
Operating ambient temperature	-20	+70	°C
Input supply voltage with respect to GND	+4.75	+5.5	V
IO voltage with respect to GND	+3.3V (± 10%)	+5.0	V

Note: IO Voltage is specified under the no-pull configuration.

4 Electrical Characteristics

4.1 DC Characteristics

The following table is measured when the control board is driving a 7" LCM @850cd/m².

Table 8. DC Characteristic

Symbol	Item	Minimum	Typical	Maximum	Unit
V _{IN}	Input power voltage	4.75	5.0	5.5	V
V _{DD}	Internal working voltage	3.0	3.3	3.5	V
	Operating current @100% backlight	920	950	–	mA
	Operating current @50% backlight	610	630	–	mA
	Operating current with backlight turned off	290	300	–	mA
V _{IH}	I/O input high level	0.7V _{DD}	–	5.0	V
V _{IL}	I/O input low level	0	–	0.3V _{DD}	V
V _{OH}	I/O output high level	V _{DD} -0.4	–	V _{DD}	V
V _{OL}	I/O output low level	0	–	0.3V _{DD}	V

5 UART Communication Protocol

The default UART communication protocol will be described in this section. However, we do allow customization of the protocol. Please contact us for the customization service.

5.1 Command Format

Table 9. Command Format

Command	Lead Byte	Type Byte	Register ID Low Byte	Register ID High Byte	Tailing Bytes
Set Register	0x11	0x00	0x00~0xFF	0x00~0xFF	Data Payload
Read Register	0x11	0x01	0x00~0xFF	0x00~0xFF	N/A

5.2 Data Payload Format

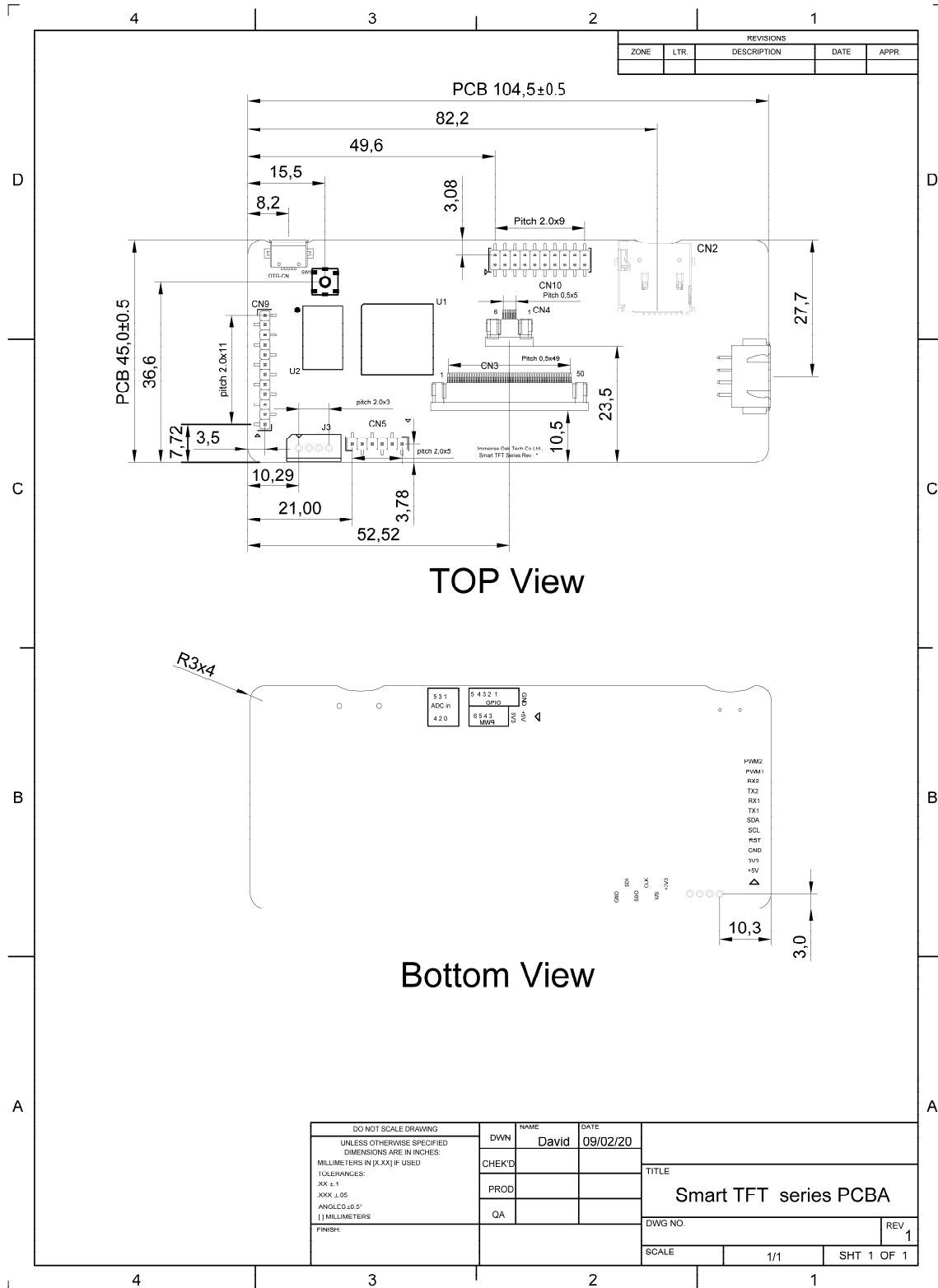
Table 10. Data Payload Format

Payload Type	ID Byte	Length Low Byte	Length High Byte	Tailing Bytes
Boolean	0x00	0x01	0x00	Data Bytes
String	0x01	0x01~0xFF	0x00~0xFF	Date Bytes
Integer	0x02	0x01/02/04/08	0x00	Data Bytes
Unsigned Integer	0x03	0x01/02/04/08	0x00	Data Bytes
Floating Point	0x04	0x04/08	0x00	Data Bytes

Note: Data bytes are of the Little-Endian format.

6 Mechanical Drawing

Figure 6. Mechanical Drawing



7 IOT Product Nomenclature

This section describes IOT's product naming rule.

Table 11. IOT Product Naming Rule

Field	1	2	3	4	5	6	7	8	9	10	11	12	13	14
# of digits	1	2	3	2	1	1	1	1	3	1	1	1	1	1
Example	I	A2	048	27	M	C	X	-	043	A	W	D	-	X

Table 12. IOT Product Nomenclature

Field	# of digits	Description
1	1	I: Representing Immense Oak Technologies' company name.
2	2	A#: Product Line Name. The first digit is an alphabet and the second is a number.
3	3	Supported horizontal resolution with ones digit dropped.
4	2	The higher two digits of the supported vertical resolution.
5	1	Product offering type. M: PCB + TFT LCM P: PCB C: Chipset
6	1	Touch panel type. R: RTP C: CTP N: No touch control.
7	1	Reserved functional identifier. Can be omitted if not needed.
8	1	Separation hyphen.
9	3	TFT LCM dimensional information. 3 digits represent the tens, ones and tenths digit of the LCM diagonal domination in inches. For examples, 10.1" => 101, 4.7" => 047
10	1	Reserved for product serial number or functional identifier.
11	1	Backlight type. S: Viewable under the sun (> 1000cd/m ²) B: Bright backlight (< 1000cd/m ² , > 600cd/m ²) W: Regular backlight (< 600cd/m ² , > 300cd/m ²)
12	1	LCD type and its characteristics. Please refer to Table 12 for details.
13	1	Separation hyphen.
14	1	Reserved.

Note: For the control board part number, Field 7 and beyond are omitted.

Table 13. Field 12 of IOT Product Nomenclature

Transmissive	Panel Type and View Angle		Working Temperature
A	TN	6H	Commercial Grade -10°C ~ 60°C
B		12H	
C		O-film	
D	IPS		
E	VA		
F	TN	6H	
G		12H	
H		O-film	
J	IPS		
K	VA		
L	TN	6H	Automobile Grade -30°C ~ 80°C
M		12H	
N		O-film	
P	IPS		
Q	VA		

Note: TN = Twisted Nematic Panel, IPS = In-Plane Switching Panel, VA = Vertical Alignment Panel