

OLED SPECIFICATION

Model No:

REP012832A

General Specification

The Features is described as follow:

■ Module dimension: 66.5 x 35.0 x 9.0(Max)

■ Active area: 55.018 x 13.098 mm

■ Dot Matrix: 128*32

Pixel size: 0.408 x 0.388 mmPixel pitch: 0.43 x 0.41 mm

■ Display Mode: Passive Matrix

■ Duty: 1/32 Duty

Display Color: White

■ IC: SSD1305Z

■ Interface: 6800, 8080, SPI, I2C

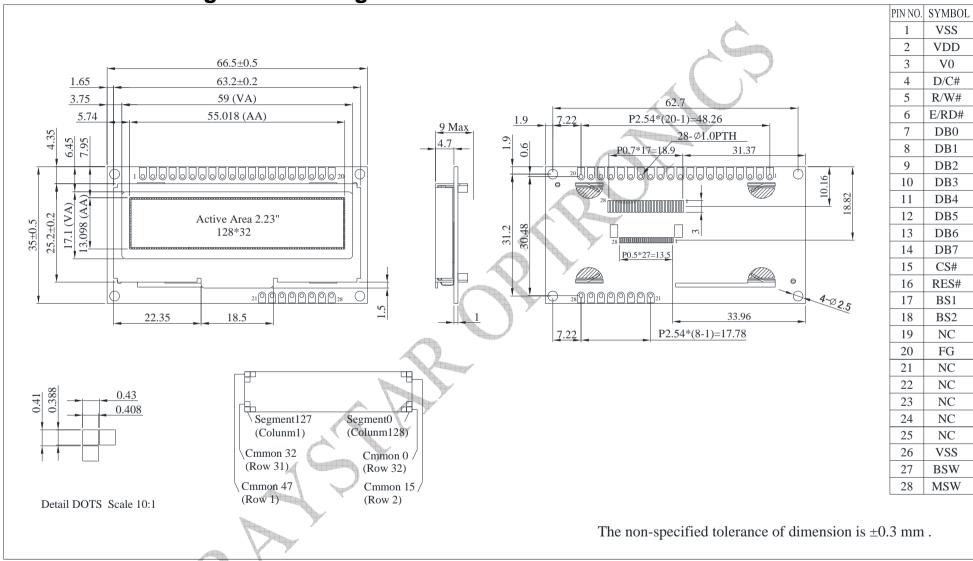
■ SIZE:2.23 inch

Interface Pin Function

No.	Symbol	Function
1	VSS	Ground.
2	VDD	Power supply pin for core logic operation.
3	V0	Power supply for panel driving voltage. This is also the most positive power voltage supply pin.
4	D/C#	This is Data/Command control pin. When it is pulled HIGH (i.e. connect to VDDIO), the data at D[7:0] is treated as data. When it is pulled LOW, the data at D[7:0] will be transferred to the command register. In I2C mode, this pin acts as SA0 for slave address selection.
5	R/W#	This is read / write control input pin connecting to the MCU interface. When interfacing to a 6800-series microprocessor, this pin will be used as Read/Write (R/W#) selection input. Read mode will be carried out when this pin is pulled HIGH (i.e. connect to VDDIO) and write mode when LOW. When 8080 interface mode is selected, this pin will be the Write (WR#) input. Data write operation is initiated when this pin is pulled LOW and the chip is selected. When serial interface is selected, this pin must be connected to VSS.
6	E/RD#	When interfacing to a 6800-series microprocessor, this pin will be used as the Enable (E) signal. Read/write operation is initiated when this pin is pulled HIGH (i.e. connect to VDDIO) and the chip is selected. When connecting to an 8080-microprocessor, this pin receives the Read (RD#) signal. Read operation is initiated when this pin is pulled LOW and the chip is selected. When serial interface is selected, this pin must be connected to VSS.
7~14	DB0~DB7	These are 8-bit bi-directional data bus to be connected to the microprocessor's data bus. When serial interface mode is selected, D0 will be the serial clock input:
15	CS#	This pin is the chip select input. (active LOW)
16	RES#	This pin is reset signal input. When the pin is LOW, initialization of the chip is executed. Keep this pin HIGH (i.e. connect to VDDIO) during normal operation.

	BS2,BS1	Communicating Protocol Select. These pins are MCU interface selection input. See the following table:						
17,18			68XX-parallel	80XX-parallel	Serial	I2C		
		BS1	0	1	0	1		
		BS2	1	1	0	0		
19	N.C.	No connection.						
20	FG(GND)	Ground.						
21~25	N.C.	No connection.						
26	VSS	Ground.						
27	BSW	Control the Piezoelectric Buzzer.						
28	MSW	Control the BLDC Vibration Motor.						

Contour Drawing & Block Diagram



Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage for Logic	VDD	-0.3	4.0	V
Supply Voltage for Display	V0	0	16.0	V
Operating Temperature	TOP	-40	+80	°C
Storage Temperature	TSTG	-40	+85	°C

Electrical Characteristics

DC Electrical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
Supply Voltage for Logic	VDD		2.8	3.0	5.0	V
Supply Voltage for Display	V0	_	11.5	12.0	12.5	V
Input High Volt.	VIH	_	0.8×VDD	_	VDD	V
Input Low Volt.	VIL	_	0	_	0.2×VDD	V
Output High Volt.	VOH	IOUT = 100uA, 3.3MHz	0.9×VDD	_	VDD	V
Output Low Volt.	VOL	IOUT = 100uA, 3.3MHz	0	_	0.1×VDD	V
Operating Current for VDD	IDD	V0 =12.0V	-	90(Ref)	120(Ref)	mA
Supply Voltage for Buzzer	BSW	VDD=5.0V 1/2 square	0.8×VDD	_	VDD	V
Supply Voltage for Vibration Motor	MSW		2.7		3.3	V