

Mechanical Data

Item	Standard Value	Unit
Module dimension	98.7 x 67.7	mm
Viewing area	92.0 x 53.0	mm
Dot Size	0.325 x 0.325	mm
Dot Pitch	0.35 x 0.35	mm

Absolute Maximum Rating

Item	Symbol	Standard Value			Unit
		min.	typ.	max.	
Power Supply	VDD-VSS	2.7	3.0	3.3	V
Input Voltage	VI	0	--	VDD	V

Note: VSS=0 Volt, VDD=3.0 Volt.

Electronical Characteristics

Item	Symbol	Condition	Standard Value			Unit
			min.	typ.	max.	
Input Voltage	VDD-VSS	-	2.7	3.0	3.3	V
Supply Current	IDD	VDD=3.0V	-	1.1	-	mA
Recommended LC Driving Voltage for Normal Temp. Version module	VDD-V5	-20°C	-	-	-	V
		25°C	-	15.5	-	
		+70°C	-	-	-	
LED Forward Voltage	VF	25°C	3.4	3.5	3.6	V
LED Forward Current	IF	25°C	86.4	96	120	mA

Feature

- 240x128 dots includes cursor
- Built-in controller UC1608
- + 3.0V power supply
- 1/128 duty cycle

Pin No.	Symbol	Description
1	VB1-	LCD Bias Voltages. These voltages are always generated internally. Connect capacitors of CB value between VBX+ to
2	VB1+	VBX+.
3	VB0-	
4	VB0+	
5	V _{LCD}	Main LCD power supply.
6	V _{BIAS}	Reference voltages to generate the actual seg driving voltages
7	V _{SS}	Ground
8	V _{DD}	Supply Voltage for logic
9	D7	Bi-directional bus for both serial and parallel host interfaces. Connect unused pins to VSS in serial modes.
10	D6	
11	D5	When PS1="L", D0: SCK (serial clock),
12	D4	D2: SDI (input data),
13	D3	D4: SDO (output data),
14	D2	D1, D3, D5-7: High impedance, connect to VSS.
15	D1	When PS1="H", D[7:0] is under the control of WR[1:0] and CS[1:0]
16	D0	
17	WR1	WR[1:0] controls the read/write operation of the host interface. In parallel mode, WR[1:0] meaning depends on whether the interface is in the 6800 mode or the 8080 mode.
18	WR0	In serial interface modes, these two pins are not used. Connect to VSS.
19	CD	Select Control data or Display data for read/write operation. When in serial 9-bit mode, this pin is not used, connect to VSS. "L": Control data "H": Display data
20	RST	When RST="L", all control registers are reinitialized by their default states and/or by their pin configurations if applicable. When RST is not used, connect the pin to VDD1.
21	CS	Chip Select. Chip selected when CE="H".
22	BM0	Bus modes:
23	BM1	Serial modes: "LL": SPI 2-bit serial mode. Parallel modes: "HL": 8080 "HH": 6800

COG type

RX240128A COG 240x128 dots

Dimension drawing

